



2509

PATENT
ATTORNEY DOCKET NO. 06802/148001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Im Cheol Ha
Serial No.: 08/998,157
Filed : December 24, 1997
Title : DECODER CIRCUIT IN A FLASH MEMORY DEVICE

Art Unit: 2509
Examiner: Trong Phan

Assistant Commissioner for Patents
Washington, DC 20231

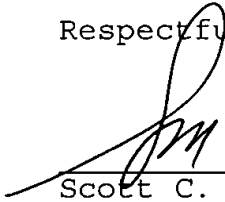
TRANSMITTAL LETTER

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050

Total Claims	<u>5</u>	-	<u>20</u>	=	<u>0</u>	\$ <u>0</u>
Independent	<u>2</u>	-	<u>3</u>	=	<u>0</u>	\$ <u>0</u>
TOTAL FEE DUE:						\$ <u>0</u>

Respectfully submitted,

Date: 1/8/99

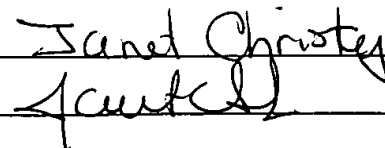


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Date of Deposit 1-8-99
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

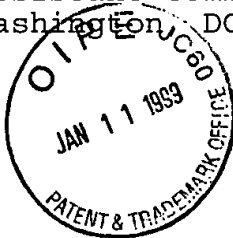


Janet Christy

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AMENDMENT

Sir:

In response to the Office Action dated October 9, 1998, paper no. 6, please amend the above-referenced application as follows:

In the Claims

Please amend claims 1, 3, 4 and 5 as follows:

1. (Amended) A decoder circuit in (a flash memory device) comprising[;]:

a global row decoder for outputting a global word line signal, said global row decoder consisted of a first decoding means selected according to a row address signal and a second decoding

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